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APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. 09/467,221 12/20/99 OKAUE T 450100-4465. **EXAMINER** MM91/0721 WILLIAM S FROMMER ESQ FELTEN, D FROMMER LAWRENCE & HAUG LLP ART UNIT PAPER NUMBER 745 FIFTH AVENUE NEW YORK NY 10151 2876 DATE MAILED: 07/21/00

Please find below and/or attached an Office communication concerning this application or proceeding.

**Commissioner of Patents and Trademarks** 



# Office Action Summary

Application No. 09/467,221 Applicant(s)

Examiner

Group Art Unit 2876

Okaue et al

**Daniel Feiten** 



🔀 Responsive to communication(s) filed on	
☐ This action is <b>FINAL</b> .	· · · · · · · · · · · · · · · · · · ·
☐ Since this application is in condition for allowance except for formal matters, in accordance with the practice under Ex parte Quay#835 C.D. 11, 453 O.G. 21	
A shortened statutory period for response to this action is set to expire3 longer, from the mailing date of this communication. Failure to respond within the papplication to become abandoned. (35 U.S.C. § 133). Extensions of time may be 37 CFR 1.136(a).	period for response will cause the
Disposition of Claim	
	is/are pending in the applicat
Of the above, claim(s)	is/are withdrawn from consideration
Claim(s)	is/are allowed.
X Claim(s) 19-24	is/are rejected.
☐ Claim(s)	is/are objected to.
☐ Claims are	e subject to restriction or election requirement.
Application Papers  ☐ See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948.  ☐ The drawing(s) filed on is/are objected to by the Ex	
<ul> <li>☐ The proposed drawing correction, filed on is ☐ ap</li> <li>☐ The specification is objected to by the Examiner.</li> <li>☐ The oath or declaration is objected to by the Examiner.</li> </ul>	proved _disapproved.
Priority under 35 U.S.C. § 119  Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).  All Some* None of the CERTIFIED copies of the priority documents have been received.  received in Application No. (Series Code/Serial Number)  received in this national stage application from the International Bureau (PCT Rule 17.2(a)).  *Certified copies not received:  Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).	
Attachment(s)	
<ul> <li>Notice of References Cited, PTO-892</li> <li>☐ Information Disclosure Statement(s), PTO-1449, Paper No(s).</li> <li>☐ Interview Summary, PTO-413</li> <li>☐ Notice of Draftsperson's Patent Drawing Review, PTO-948</li> <li>☐ Notice of Informal Patent Application, PTO-152</li> </ul>	
SEE OFFICE ACTION ON THE FOLLOWING PAGES	

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Art Unit: 2876

Representative: Frommer, W. (25,506)

## **DETAILED ACTION**

1. Receipt of the Preliminary Amendment filed April 17, 2000 canceling claims 1-18 and

adding claims 19-24.

# Specification

- 5 2. The abstract of the disclosure is objected to because the abstract should be a single
- paragraph. Correction is required. See MPEP § 608.01(b).

## Claim Rejections - 35 USC § 102

- 10 3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form
- 11 the basis for the rejections under this section made in this Office action:
- A person shall be entitled to a patent unless --
- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 15 3. Claims 19, 20, and 22-23 are rejected under 35 U.S.C. 102(b) as being anticipated by
- 16 Robinson et al (US 5,428,579).

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Re claim 19, Robinson et al discloses a memory card 110 for storing data transmitted 1 Is from an external apparatus 101 (see Robinson et al fig.2, col. 1, lines 26-33; and col. 5, lines 2 1-8; and col. 5, line 62 to col. 6, line 2); 3 a flash memory (EPROMs) for storing the data transmitted from the external apparatus 101 (see col. 5, line 66-68); 5 a switch 116 settable to a state which inhibits writing data into the flash memory б (EPROMs) (see col. 6, lines 15-21); 7 an interface 112 for transmitting data to and receiving data from the external 8 apparatus 101 (see col. 5, lines 62-63); and 9 control means (EPROM) for controlling said memory card in accordance with an 10 instruction transmitted from said external apparatus (see Robinson et al. col 6, lines 15-22), the 11 control means sending to the external apparatus 101 via the interface 112 the state of the switch 12 116 in response to an instruction transmitted thereto via the interface 112 from the external 13 apparatus 116 (see Robinson et al, col. 5, lines 56 to col. 6, line 2). 14 15 Re claim 20 and 23, a memory card, wherein the interface includes nine connectors at 16

least one of which transmits and receives data (see Robinson et al, fig. 3, col. 6, line 27-34).

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Re claim 22, a system comprising a memory card 110 and an external apparatus 10 1, wherein data is communicated therebetween,

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the memory card 101 comprising:

a flash memory (EPROMs) for storing the data transmitted from the external

apparatus 101 (see Robinson et al, col. 5, line 66-68);

a switch 116 settable to a state which inhibits writing data into said flash memory

(see Robinson et al, col. 6, line 64 to col. 7, line 7);

an interface 116 for transmitting data to and receiving data from the external

apparatus 101 (see Robinson et al, col. 5, lines 62-63); and

control means (EPROM) for controlling the memory card in accordance with an

instruction transmitted from the external apparatus 101 (see Robinson et al, col, lines 15-22), the

control means (EPROMs) sending to the external apparatus 101 via the interface 112 the state of

the switch 116 in response to an instruction transmitted thereto via said interface from the

external apparatus 116 (see col. 5, lines 56 to col. 6, line 2);

and the external apparatus comprising:

a controller (EPROM) for writing data to or erasing data from the flash memory of

the memory card (see col. 5, lines 56-61); the controller transmitting an instruction to the

memory card via the interface to determine whether a data writing operation to the flash memory

of the memory card in inhibited (see col. 7, line 57 to col. 8 line 5)

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#### Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all

obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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- 5. Claims 21 and 24 are rejected under 35 U.S.C. 103(a) as being unpatentable over Robinson et al (US 5,428,579) in view of Jigour et al (US 5,815,426). The teachings of
- Robinson et al have been discussed above. 13

Robinson et al fails to explicitly teach or disclose a memory card wherein the data is received from and transmitted to the external apparatus in serial form.

Jigour et al discloses/teaches memory cards (see Jigour et al fig. 23) wherein the data can be received from and transmitted to the external apparatus in parallel and/or serial form (see col. 10 18 lines 38-53). Serial form of transmitting/receiving data is conventional in the art for cheaply transferring digital information one bit at a time in the order the values were stored during the initial entry. It would have therefore been obvious for an artisan of ordinary skill at the time of the invention of Robinson et al under normal engineering practices to employ a device to receive data from and transmitted data to the external apparatus in serial form, as

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Is disclosed/taught in Jigour et al., in order to maintain low cost of data transfer between the

memory card and the external device. Thus such a modification would have been an obvious

expedient to one of ordinary skill in the art.

Conclusion

8 6. The prior art made of record and not relied upon is considered pertinent to applicant's

disclosure: Robinson et al (US 5,388,248) discloses a flash memory card including plural flash

memories and circuitry for selectively outputting ready/busy signals in different operating modes

11 Elbert (US 5,267,218) discloses nonvolatile memory card with a single power supply a input Baji

et al (US 5,163,111) discloses customized personal terminal device 10 Nagata et al (US

5,796,092) discloses and IC card and IC card system m 7. Any inquiry concerning this

communication or earlier communications from the examiner should be directed to Daniel S.

15 Felten whose telephone number is (703) 305-0724.

The examiner can normally be reached between the hours of 7:30AM to 6:OOPM

17 Monday, Tuesday, Thursday, and Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Don Hajec, can be reached on (703) 308-7045. The fax phone number for this Group

is (703)308-7382 or (703) 308-7722.

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Communications via Internet e-mail regarding this application, other than those under 35 1

- U.S.C. 132 or which otherwise require a signature, may be used by the applicant and should be 2
- addressed to [daniel.felten@uspto.gov]. 3
- All Internet e-mail communications will be made of record in the application file. PTO
- employees do not engage in Internet communications where there exists a possibility that 5
- sensitive information could be identified or exchanged unless the record includes a properly б
- signed express waiver of the confidentiality requirements of 35 U.S.C. 122. This is more clearly 7
- set forth in the Interim Internet Usage Policy published in the Official Gazette of the Patent and 8
- Trademark on February 25, 1997 at 1 195 OG 89. 9

Any inquiry of a general nature or relating to the status of this application or proceeding 10

should be directed to the Group receptionist whose telephone number is (703) 308-0956. 11

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June 15, 2000

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